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| ChengHao\_MEC733\_W2022\_Lab2.ino | ChengHao\_MEC733\_W2022\_Lab2.h | ChengHao\_MEC733\_W2022\_Lab2.s |
| Int main()  {  uint8\_t i,j;  port\_D\_init();  delay\_ms(2000);  initUSART();  printString("We are doing lab 2!");  while(1)  {  i=port\_D7\_read();  printString("\n i=");  printBinaryByte(i);  j=port\_D6\_read();  printString("\n j=");  printBinaryByte(j);    if(i==0)  {port\_D3\_high();}  if(i!=0)  {port\_D3\_low();}    if(j==0)  {port\_D2\_high();}  if(j!=0)  {port\_D2\_low();}  }  } | extern "C" void port\_D\_init(void);  extern "C" uint8\_t port\_D6\_read(void);  extern "C" uint8\_t port\_D7\_read(void);  extern "C" void port\_D2\_high(void);  extern "C" void port\_D2\_low(void);  extern "C" void port\_D3\_high(void);  extern "C" void port\_D3\_low(void); | port\_D\_init  0b00001100 => R16 => 0x0A  0b11000000 => R16 => 0X0B  port\_D7\_read  0x09 => R24  0b10000000 & R24  port\_D6\_read  0x09 => R24  0b10000000 & R24  port\_D2\_high  0b00000100 => 0x0B  port\_D2\_low  0b00000000 => 0x0B  port\_D3\_high  0b00001000 => 0x0B  port\_D3\_low  0b00000000 => 0x0B |